

## REFERENCE VOLTAGE GENERATING CIRCUIT USING ACTIVE RESISTANCE DEVICE

### ABSTRACT OF THE DISCLOSURE

5           A reference voltage generating circuit includes a current mirror circuit having first  
and second current paths formed between a first power source terminal and a second  
power source terminal in which the current mirror circuit is operated in response to a  
voltage level of the second current path, a reference voltage output node for providing a  
reference voltage and being located on the second current path, an active resistance  
10       device formed on the first current path to be operated in a linear region of a current-  
voltage characteristic curve of the active resistance device, and a voltage supply circuit  
for supplying the active resistance device with an enable voltage to control the active  
resistance device to be operated in the linear region.